

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No:

09/903,780

Filed:

July 12, 2001

For: METHOD AND SYSTEM FOR IMPROVING NETWORK PERFORMANCE ENHANCING PROXY ARCHITECTURE WITH GATEWAY

REDUNDANCY

Art Unit:

2681

Examiner:

UNKNOWN

03 C

Customer No.: 020991

PATENT

## RESPONSE RE NOTICE TO FILE CORRECTED APPLICATION PAPERS

Asst. Commissioner for Patents Washington, D.C. 20231

Sir:

In response to the Notice to File Corrected Application papers mailed August 27, 2001, (hereinafter "Notice") enclosed is the Abstract in the above-identified application.

Applicant hereby petitions for an extension of time for 0 months to respond to the Notice in accordance with the provisions of 37 CFR 1.136(a). If an additional extension of time is required, please consider this a petition therefor.

Respectfully submitted,

John T. Whelan Reg. No. 32,448

Highes Electronics Corporation Bldg. 001, M.S. A109 P.O. Box 956 El Segundo, CA 90245-0956 301-428-7172

I hereby certify that the above-identified papers are being deposited with the United States Postal Service via First Class Mail to the United States Patent and Trademark Office, Washington, D.C. 20231 on: September 14, 2001.